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Substitute for form 1449/PTO				Complete if Known	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(Use as many sheets as necessary)</i>				Application Number	
				Filing Date	November 11, 2003
				First Named Inventor	William J. Borland
				Art Unit	3729
				Examiner Name	D. Nguyen
Sheet	1	of	2	Attorney Docket Number	EL 0509 US NA

U. S. PATENT DOCUMENTS

## FOREIGN PATENT DOCUMENTS

Examiner Signature		Date Considered	
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**\*EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>See Kind Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.

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				Application Number	
				Filing Date	November 11, 2003
				First Named Inventor	William J. Borland
				Art Unit	3729
				Examiner Name	Da NGUYEN
Sheet	2	of	2	Attorney Docket Number	EL 0509 US NA

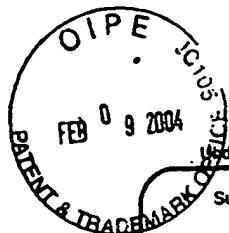
NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T <sup>2</sup>
DN		FELTEN, J., FERGUSON, S., Embedded Ceramic Resistors and Capacitors in PWB - Process and Design, IPC Printed Circuits Expo, Long Beach, California, March 2002			<input type="checkbox"/>
DN		FELTEN, J., SNOGREN, R., AND ZHOU, J., Embedded Ceramic Resistors and Capacitors in PWB: Process and Performance, Fall IPC Meeting, Orlando, Florida, October 11, 2001			<input type="checkbox"/>
DN		BORLAND, W., FELTEN, J., Thick Film Ceramic Capacitors and Resistors inside Printed Circuit Boards, 34th International Symposium on Microelectronics (IMAPS), Baltimore, MD., October 9-11, 2001			<input type="checkbox"/>
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					<input type="checkbox"/>
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Examiner Signature	<i>Da Nguyen</i>	Date Considered	<i>11/11/03</i>
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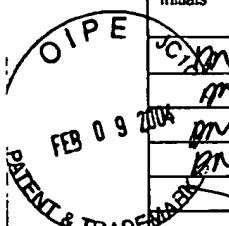
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**Complete if Known**

Application Number	10/718,044
Filing Date	November 20, 2003
First Named Inventor	William J. Borland
Art Unit	3729
Examiner Name	D. NGUYEN
Attorney Docket Number	EL0509 US NA



**U. S. PATENT DOCUMENTS**

## FOREIGN PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)			
DR		JP 2003-92460	05-31-2002		
DR		JP 2-153589	06-13-1990	Ibiden KK	
DR		JP 2001-168491	12-09-1999		

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INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet

2

of

3

## Complete If Known

Application Number	10/718,044
Filing Date	November 20, 2003
First Named Inventor	William J. Borland
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Examiner Name	D. NGUYEN

Attorney Docket Number EL0509 US NA

## NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
DN		JOHN FELTEN AND SAUL FERGUSON, Embedded Ceramic Resistors and Capacitors for PWB, IP Printed Circuit Expo, San Diego, CA, April 2000.	<input type="checkbox"/>
DN		JOHN J. FELTEN AND SAUL FERGUSON, Ceramic Resistors and Capacitors Embedded in PWB, IPC, San Diego, April 5, 2000.	<input type="checkbox"/>
DN		JOHN J. FELTEN AND SAUL FERGUSON, Ceramic Resistors and Capacitors Embedded in PWB, IMAPS, Denver, April 29, 2000.	<input type="checkbox"/>
DN		JOHN J. FELTEN AND WILLIAM J. BORLAND, Embedded Ceramic Passives in PWB: Process Development, IPC Printed Circuit Expo, Anaheim, CA, April 2001.	<input type="checkbox"/>
DN		JOHN J. FELTEN AND WILLIAM J. BORLAND, Ceramic Resistors and Capacitors Embedded in PWB's, IPC Expo, April 3, 2001.	<input type="checkbox"/>
DN		WILLIAM BORLAND, Designing for Embedded Passives, Printed Circuit Design, August 2001.	<input type="checkbox"/>

Examiner Signature

*D. Nguyen*

Date Considered

11/11/05

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				Filing Date	November 20, 2003
				First Named Inventor	William J. Borland
				Art Unit	3729
				Examiner Name	D. NGUYEN
Sheet	3	of	3	Attorney Docket Number	EL0509 US NA

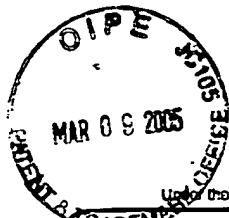
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DN		DR. KIM FJELDSTED AND STACEY L. CHASE, Embedded Passives: Laser Trimmed Resistors, CircuitTree, Volume 70, March 2002.			<input type="checkbox"/>
DN		WILLIAM J. BORLAND AND SAUL FERGUSON, Embedded Passive Components in Printed Wiring Boards, a Technology Review, To be published in CircuiTree Magazine, 2001.			<input type="checkbox"/>
DN		JIMING ZHOU, JOHN D. MYERS AND JOHN J. FELTEN, Embedded Passives Technology for PCBs: Materials, Design, and Process, IMAPS 2002 Conference, Denver, CO, Sept. 4-6, 2002.			<input type="checkbox"/>
DN		JOHN J. FELTEN, Electronic Circuits World Convention, Paper number IPC31, Advanced Embedded Passives Technologies - Putting Ceramic Components into Organic PWBs.			<input type="checkbox"/>
DN		WILLIAM BORLAND, JOHN J. FELTEN, SAUL FERGUSON, ALTON B. JONES AND ANGELA A. LAWRENCE, Embedded Singulated Ceramic Passives in Printed Wiring Boards, IMAPS Advanced Technology Workshop on Passive Integration, Ogunquit, ME, June 19-21, 2002.			<input type="checkbox"/>
DN		RICHARD C. SNOGREN, Embedded Passives, a Review, IPC Designers Learning Symposium, October 3, 2002, Toronto ON, CN.			<input type="checkbox"/>

Examiner Signature	<i>D. Nguyen</i>	Date Considered	11/11/05
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#### Sheets

1

6

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Application Number	10/718,044
Filing Date	November 20, 2003
First Named Inventor	William J. Borland E
Art Unit	2844 3729
Examiner Name	D. NGUYEN
Attorney Docket Number	EL 05091 ISNA

U. S. PATENT DOCUMENTS

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		Foreign Patent Document	Publication Date MM-DD-YYYY			
DN		EP 1 265 466 A	12-11-2002	Dai Nippon Printing Co. Ltd.		
MV		EP 1 307 079 A	05-02-2003	Adtec Engineering Co., Ltd.		

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				Application Number	10/718044
				Filing Date	November 20, 2003
				First Named Inventor	William J. Borland Et. Al.
				Art Unit	2844 3729
				Examiner Name	D. NGUYEN
Sheet	2	of	2	Attorney Docket Number	EL0509USNA

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DN		Patent Abstracts of Japan, Vol. 1998, No. 06, 30 April 1998 (1998-04-30) & JP 10 051143 A (Oki Purintetsudo Circuit KK), 20 February 1998 (1998-02-20), Abstract				<input type="checkbox"/>
DN		Patent Abstracts of Japan, Vol. 2000, No. 02, 29 February 2000 (2000-02-29) & JP 11 330316 A (Nec Tohoku Ltd), 30 November 1999 (1999-11-30), Abstract				<input type="checkbox"/>
DN		Patent Abstracts of Japan, Vol. 2000, No. 22, 9 March 2001 (2001-03-09) & JP 2001 135941 A (Matsushita Electric Works Ltd.), 18 May 2001 (2001-05-18), Abstract				<input type="checkbox"/>
DN		Patent Abstracts of Japan, Vol. 2000, No. 16, 8 May 2001 (2001-05-08) & JP 2001 022099 A (Adtec Engineering Co. Ltd.), 26 January 2001 (2001-01-26), Abstract				<input type="checkbox"/>
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